

in response to receipt from the host processor of user data and a command to write said user data to said at least one mass memory storage block address, writing at least one sector of said user data into the addressed at least one of the memory array cell groups, and

in response to receipt from the host processor of a command to read user data from said at least one mass memory storage block address, reading at least one sector of said user data and associated overhead data from the addressed at least one of the memory array cell groups.

~~21~~ ~~22~~ ~~23~~ ~~24~~ ~~25~~ ~~26~~ ~~27~~ ~~28~~ ~~29~~ ~~30~~ ~~31~~ ~~32~~ ~~33~~ ~~34~~ ~~35~~ ~~36~~ ~~37~~ ~~38~~ ~~39~~ ~~40~~ ~~41~~ ~~42~~ ~~43~~ ~~44~~ ~~45~~ ~~46~~ ~~47~~ ~~48~~ ~~49~~ ~~50~~ ~~51~~ ~~52~~ ~~53~~ ~~54~~ ~~55~~ ~~56~~ ~~57~~ ~~58~~ ~~59~~ ~~60~~ ~~61~~ ~~62~~ ~~63~~ ~~64~~ ~~65~~ ~~66~~ ~~67~~ ~~68~~ ~~69~~ ~~70~~ ~~71~~ ~~72~~ ~~73~~ ~~74~~ ~~75~~ ~~76~~ ~~77~~ ~~78~~ ~~79~~ ~~80~~ ~~81~~ ~~82~~ ~~83~~ ~~84~~ ~~85~~ ~~86~~ ~~87~~ ~~88~~ ~~89~~ ~~90~~ ~~91~~ ~~92~~ ~~93~~ ~~94~~ ~~95~~ ~~96~~ ~~97~~ ~~98~~ ~~99~~ ~~100~~ ~~101~~ ~~102~~ ~~103~~ ~~104~~ ~~105~~ ~~106~~ ~~107~~ ~~108~~ ~~109~~ ~~110~~ ~~111~~ ~~112~~ ~~113~~ ~~114~~ ~~115~~ ~~116~~ ~~117~~ ~~118~~ ~~119~~ ~~120~~ ~~121~~ ~~122~~ ~~123~~ ~~124~~ ~~125~~ ~~126~~ ~~127~~ ~~128~~ ~~129~~ ~~130~~ ~~131~~ ~~132~~ ~~133~~ ~~134~~ ~~135~~ ~~136~~ ~~137~~ ~~138~~ ~~139~~ ~~140~~ ~~141~~ ~~142~~ ~~143~~ ~~144~~ ~~145~~ ~~146~~ ~~147~~ ~~148~~ ~~149~~ ~~150~~ ~~151~~ ~~152~~ ~~153~~ ~~154~~ ~~155~~ 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either writing user data to, or reading user data from, said at least one non-volatile memory sector,

either writing to, or reading from, said at least one non-volatile memory sector, overhead data related either to said at least one non-volatile memory sector or to user data stored in said at least one non-volatile memory sector, and

wherein the writing of user and overhead data includes programming the individual memory cells of the array into said one of more than two distinct threshold level ranges.

¹⁰⁰ ~~162~~. The method of claim ⁹⁹ ~~161~~, wherein the overhead data written into said at least one non-volatile memory sector is generated within the memory controller.

¹⁰¹ ~~163~~. The method of claim ⁹⁹ ~~161~~, wherein the user data written into the individual sectors is substantially 512 bytes.

¹⁰² ~~164~~. The method of claim ⁹⁹ ~~161~~, wherein the overhead data stored in said overhead portion of the individual sectors includes addresses of the individual sectors.

¹⁰³ ~~165~~. The method of claim ⁹⁹ ~~164~~, wherein the overhead data stored in said overhead portion of the individual sectors additionally includes, in those sectors that are defective, addresses of sectors being substituted therefor.

¹⁰⁴ ~~166~~. The method of claim ⁹⁹ ~~161~~, additionally comprising causing the controller to identify and store addresses of any defective non-volatile memory sectors within the array, and, wherein designating an address of said at least one memory sector includes, in response to designating an address of a defective sector, substituting an address of another sector instead.

¹⁰⁵ ~~167~~. The method of claim ⁹⁹ ~~161~~, wherein the address of said at least one mass memory storage block includes an address of at least one magnetic disk sector.

¹⁰⁶ ~~168~~. The method of claim ⁹⁹ ~~161~~, further comprising erasing data from a selected at least one of the individual sectors of memory cells by simultaneously applying an erase voltage to all of the memory cells within said selected at least one sector, thereby to simultaneously erase any user data and associated overhead data contained in said at least one sector.

¹⁰⁷ ~~169~~. The method of claim ⁹⁹ ~~168~~, wherein the memory array cells individually include erase gates, and the erase voltage is simultaneously applied to the erase gates of said selected at least one sector of memory cells.--